

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	4	("6473727" "5784382" "5467358" "5224101").pn.	USPAT	OR	OFF	2006/10/30 09:53
S2	50	(US-20050015778-\$ or US-20020092002-\$).did. or (US-6185629-\$ or US-6058473-\$ or US-6115131-\$ or US-6397379-\$ or US-4439830-\$ or US-5210870-\$ or US-5530883-\$ or US-5537622-\$ or US-5537604-\$ or US-5537603-\$ or US-5548769-\$ or US-5590362-\$ or US-5619713-\$ or US-6405111-\$ or US-5269017-\$ or US-5945986-\$ or US-4347565-\$ or US-5694348-\$ or US-5905881-\$ or US-6016538-\$ or US-6032170-\$ or US-6067613-\$ or US-6098163-\$ or US-6116768-\$ or US-6173305-\$ or US-6240437-\$).did. or (US-6442663-\$ or US-4253145-\$ or US-4494189-\$ or US-4521846-\$ or US-4774659-\$ or US-4967342-\$ or US-5280592-\$ or US-5325520-\$ or US-5339417-\$ or US-5459872-\$ or US-5574936-\$ or US-5577231-\$ or US-5794256-\$ or US-5819063-\$ or US-5949985-\$ or US-6038631-\$ or US-6105041-\$ or US-6125443-\$ or US-6247144-\$ or US-6798713-\$ or US-7065750-\$ or US-6154826-\$).did.	US-PGPUB; USPAT	OR	OFF	2006/10/30 08:43
S3	201	maintenance adj bus and hardware	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 09:54
S4	1403	configuration near3 status near3 register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 10:52
S5	8	S3 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 10:03

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S6	6129	(configuration near3 status near3 register) csr	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 10:03
S7	12	S6 and S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 10:04
S8	6020	service adj processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 10:04
S9	70	S8 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 10:04
S10	4	S9 and S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 10:04
S11	3243	(control configuration) adj status adj register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 11:12
S12	20	((control configuration) adj status adj register) csr same (service adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 10:58
S13	15841	((control configuration) adj status adj register) control adj word	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 11:13

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S14	710	((control configuration) adj status adj register) control adj word) with (validat\$4 test\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 11:13
S16	8	((control configuration) adj status adj register) control adj word) with (validat\$4 test\$4) same (language)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 11:14
S17	14	((control configuration) adj status adj register) control adj word) with (validat\$4 test\$4) same (language model)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 11:20
S18	49	((control configuration) adj status adj register) control adj word) same (validat\$4 test\$4) same (language model)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 12:02
S19	31	(test\$4 validat\$4) with (hardware component) and (hardware adj language)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 12:33
S20	6	(test\$4 validat\$4) with (hardware component) and (hardware adj language) and ((control configuration) with status with register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 12:37
S21	1709	(test\$4 validat\$4) with (hardware component)and ((control configuration) with status with register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 12:37
S22	1	(test\$4 validat\$4) with (hardware component)and ((control configuration) with status with register) and abstract adj language	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 12:38

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S23	1	(test\$4 validat\$4) with (hardware component)and ((control configuration) with status with register) and abstract near3 language	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 12:37
S24	208	(test\$4 validat\$4) with (hardware component)and ((control configuration) with status with register) and hardware with language	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:12
S25	1	(test\$4 validat\$4) with (hardware component)and ((control configuration) with status with register) and hardware with language with abstract	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 12:38
S26	5	(test\$4 validat\$4) with (hardware component)and ((control configuration) with status with register) and hardware with language with abstract\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 12:38
S27	87	(test\$4 validat\$4) with (hardware component)and ((control configuration) with status with register) and hardware with language and function with call	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:13
S28	1	(test\$4 validat\$4) with (hardware component)and ((control configuration) with status with register) and hardware with language and function with call with specific	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:14
S29	9	(test\$4 validat\$4) with (hardware component)and ((control configuration) with status with register) and hardware with language and function with call with hardware	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:14
S30	9	(test\$4 validat\$4) with (hardware component)and ((control configuration) with status with register) and hardware with language and function with call\$4 with hardware	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:34

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S31	696	(test\$4 validat\$4) with ((control configuration) with status with register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:35
S32	157	(test\$4 validat\$4) near5 ((control configuration) with status with register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:35
S33	47	(test\$4 validat\$4) near5 ((control configuration) with status with register) and hardware with (transaction manipulat\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:36
S35	1	(test\$4 validat\$4) near5 ((control configuration) with status with register) and hardware with (transaction manipulat\$5) same state	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:36
S36	2	(test\$4 validat\$4) near5 ((control configuration) with status with register) and hardware with (transaction manipulat\$5) with (instructions program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:38
S37	2	(test\$4 validat\$4) near5 ((control configuration) with status with register) and hardware with (transaction manipulat\$5) with (instructions program code)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:40
S38	4	(test\$4 validat\$4) near5 ((control configuration) with status with register) and hardware with (transaction manipulat\$5) and hardware with (specific info information)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:44
S44	17	(diagnos\$4) with ((control configuration) with status with register) and hardware with (transaction manipulat\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:51

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S45	2	manipulating adj hardware near5 state	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:52
S46	3	manipulating adj hardware with (control status configuration) near5 register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:53
S47	112	(state near3 hardware) with (control status configuration) near5 register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 13:54
S48	17	(code instructions program) with (state near3 hardware) with ((control status configuration) near5 register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 14:02
S49	69	verify\$4 near5 ((control status configuration) near5 register) with (procedure operation performance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 14:02
S50	4	verify\$4 near5 ((control status configuration) near5 register) with (procedure operation performance) same (hardware specific)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 14:03
S51	2	"20040215440".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 16:29
S54	307	maintenance adj bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 10:33

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S55	19	maintenance adj bus and service adj processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 10:34
S56	0	diagnos\$4 same maintenance adj bus same serive adj processor and (test\$4 verify\$4 validat\$4 diagnos\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 10:34